

μA709

HIGH PERFORMANCE OPERATIONAL AMPLIFIER

FAIRCHILD LINEAR INTEGRATED CIRCUITS

GENERAL DESCRIPTION — The μA709 is a monolithic High Gain Operational Amplifier constructed using the Fairchild Planar* epitaxial process. It features low offset, high input impedance, large input common mode range, high output swing under load and low power consumption. The device displays exceptional temperature stability and will operate over a wide range of supply voltages with little performance degradation. The amplifier is intended for use in dc servo systems, high impedance analog computers, low level instrumentation applications and for the generation of special linear and nonlinear transfer functions.

ABSOLUTE MAXIMUM RATINGS

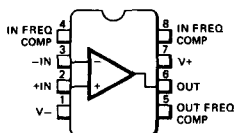
Supply Voltage	±18 V
Internal Power Dissipation (Note)	
Metal Can	500 mW
DIP	670 mW
Flatpak	570 mW
Differential Input Voltage	±5.0 V
Input Voltage	±10 V
Storage Temperature Range	
Metal, Hermetic DIP, and Flatpak	−65°C to +150°C
Molded DIP	−55°C to +125°C
Operating Temperature Range	
Military (μA709A and μA709)	−55°C to +125°C
Commercial (μA709C)	0°C to +70°C
Lead Temperature	
Metal Can, Hermetic DIP, and Flatpak (Soldering 60 s)	300°C
Molded DIP	260°C
Output Short Circuit Duration	5 s

NOTE:

Rating applies to ambient temperature up to 70°C. Above 70°C ambient derate linearly at 6.3mW/°C for Metal Can, 8.3mW/°C for DIP, 7.1mW/°C for the Flatpak and 5.6mW/°C for the Mini DIP.

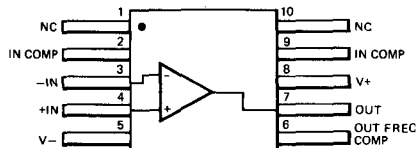
CONNECTION DIAGRAMS

8-LEAD MINI DIP
(TOP VIEW)
PACKAGE OUTLINE 9T
PACKAGE CODE T



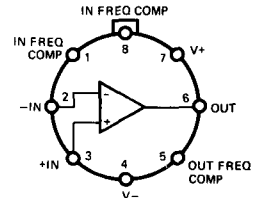
ORDER INFORMATION	
TYPE	PART NO.
μA709C	μA709TC

10-LEAD FLATPAK
(TOP VIEW)
PACKAGE OUTLINE 3F
PACKAGE CODE F



ORDER INFORMATION	
TYPE	PART NO.
μA709A	μA709AFM
μA709	μA709FM

CONNECTION DIAGRAMS
8-LEAD METAL CAN
(TOP VIEW)
PACKAGE OUTLINE 5S
PACKAGE CODE H



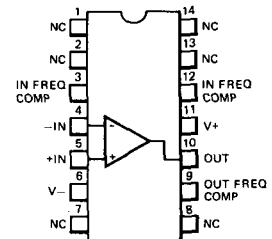
NOTE: Pin 4 connected to case

ORDER INFORMATION

TYPE	PART NO.
μA709A	μA709AHM
μA709	μA709HM
μA709C	μA709HC

14-LEAD DIP
(TOP VIEW)

PACKAGE OUTLINE 6A 9A
PACKAGE CODE D P



ORDER INFORMATION	
TYPE	PART NO.
μA709A	μA709ADM
μA709	μA709DM
μA709C	μA709DC
μA709C	μA709PC

*Planar is a patented Fairchild process.

FAIRCHILD LINEAR INTEGRATED CIRCUITS • $\mu A709$

$\mu A709$

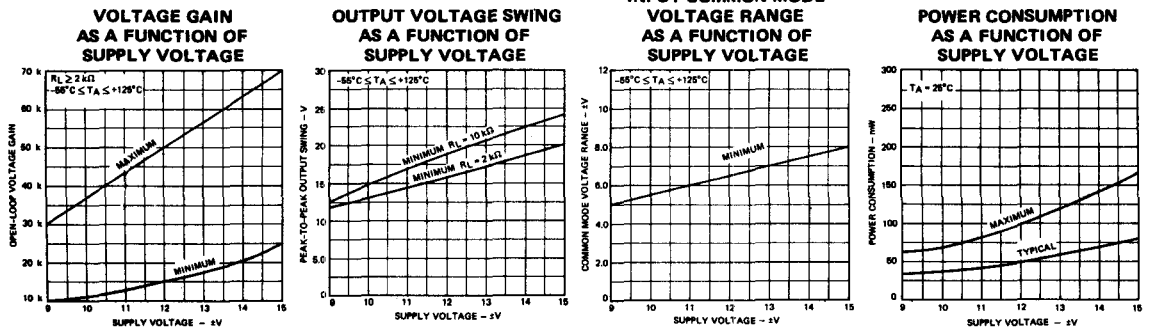
ELECTRICAL CHARACTERISTICS ($T_A = +25^\circ\text{C}$, $\pm 9\text{ V} \leq V_S \leq \pm 15\text{ V}$ unless otherwise specified)

PARAMETER (see definitions)	CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage	$R_S \leq 10\text{ k}\Omega$		1.0	5.0	mV
Input Offset Current			50	200	nA
Input Bias Current			200	500	nA
Input Resistance		150	400		k Ω
Output Resistance			150		Ω
Power Consumption	$V_S = \pm 15\text{ V}$		80	165	mW
Transient Response	$V_{IN} = 20\text{ mV}$, $R_L = 2\text{ k}\Omega$, $C_1 = 5000\text{ pF}$, $R_1 = 1.5\text{ k}\Omega$, $C_2 = 200\text{ pF}$, $R_2 = 50\Omega$		0.3	1.0	μs
		Overshoot		10	30

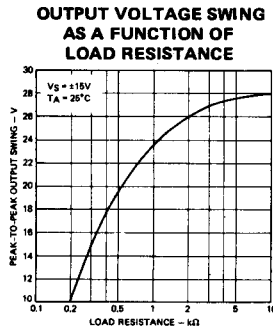
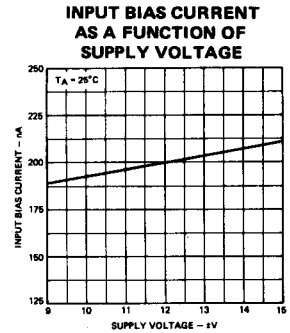
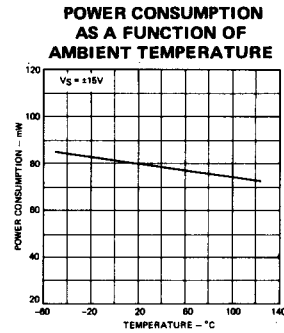
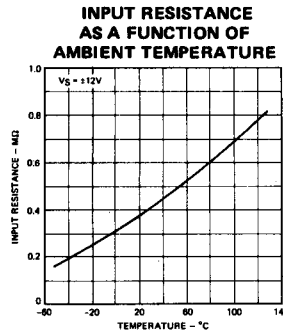
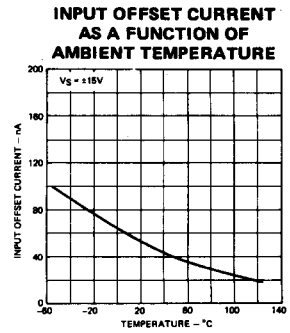
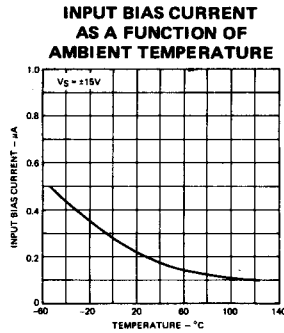
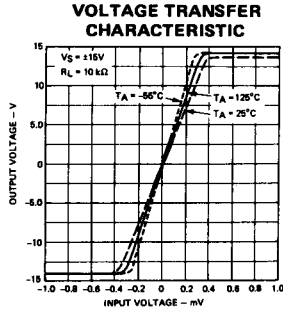
The following specifications apply for $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$:

Input Offset Voltage	$R_S \leq 10\text{ k}\Omega$			6.0	mV
Average Temperature Coefficient of Input Offset Voltage	$R_S = 50\Omega$		3.0		$\mu\text{V}/^\circ\text{C}$
	$R_S \leq 10\text{ k}\Omega$		6.0		$\mu\text{V}/^\circ\text{C}$
Large Signal Voltage Gain	$V_S = \pm 15\text{ V}$, $R_L \geq 2\text{ k}\Omega$, $V_{OUT} = \pm 10\text{ V}$	25,000	45,000	70,000	V/V
Output Voltage Swing	$V_S = \pm 15\text{ V}$, $R_L \geq 10\text{ k}\Omega$	± 12	± 14		V
	$V_S = \pm 15\text{ V}$, $R_L \geq 2\text{ k}\Omega$	± 10	± 13		V
Input Voltage Range	$V_S = \pm 15\text{ V}$	± 8.0	± 10		V
Common Mode Rejection Ratio	$R_S \leq 10\text{ k}\Omega$	70	90		dB
Supply Voltage Rejection Ratio	$R_S \leq 10\text{ k}\Omega$		25	150	$\mu\text{V}/\text{V}$
Input Offset Current	$T_A = +125^\circ\text{C}$		20	200	nA
	$T_A = -55^\circ\text{C}$		100	500	nA
Input Bias Current	$T_A = -55^\circ\text{C}$		0.5	1.5	μA
Input Resistance		40	100		k Ω

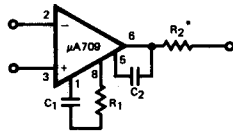
PERFORMANCE CURVES FOR $\mu A709$



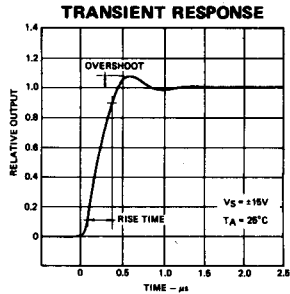
TYPICAL PERFORMANCE CURVES FOR $\mu A709$ AND $\mu A709C$



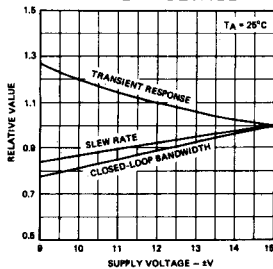
FREQUENCY COMPENSATION CIRCUIT



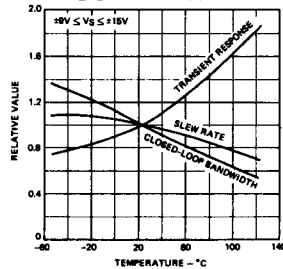
* Use $R_2 = 50 \Omega$ when the amplifier is operated with capacitive loading.



FREQUENCY CHARACTERISTICS AS A FUNCTION OF SUPPLY VOLTAGE

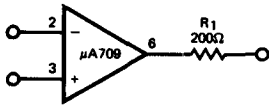


FREQUENCY CHARACTERISTICS AS A FUNCTION OF AMBIENT TEMPERATURE

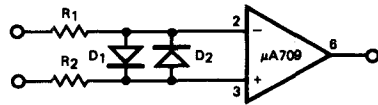


PROTECTION CIRCUITS

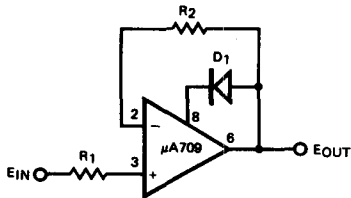
OUTPUT
SHORT CIRCUIT PROTECTION



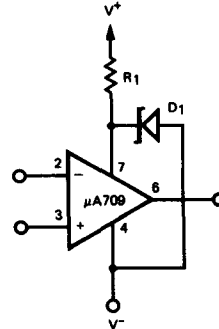
INPUT
BREAKDOWN PROTECTION



LATCH-UP PROTECTION



SUPPLY
OVERVOLTAGE PROTECTION



Pin numbers apply to metal can or mini DIP package only.

EQUIVALENT CIRCUIT

